

**IN THE CLAIMS:**

Please cancel claims 1-3 and 8-9 without prejudice to or disclaimer of the subject matter recited therein.

Please amend claim 4 as follows:

**LISTING OF CURRENT CLAIMS**

Claims 1-3. (Canceled)

4. (Currently Amended) A device for setting a clock, which is a clock generating device of a computer motherboard, comprising:

a crystal oscillator;

a frequency control unit;

5 a phase-lock-loop (PLL) spread-spectrum unit electrically connected with the crystal oscillator and the frequency control unit;

a memory unit having a clock setting value stored therein;

a detection control unit electrically connected with the memory unit and used to detect a signal status; and

10 a logic control unit electrically connected with the PLL spread-spectrum unit, the frequency control unit and the detection control unit,

wherein the clock setting value stored in the memory unit is an operating clock value of the logic control unit stored in the memory unit by the detection control unit when the detection control unit detects a BIOS as working regularly.

5. (Original) The device as claimed in the claim 4, wherein the memory unit is an electrically erasable programmable read-only memory (EEROM), an erasable programmable read-only memory (EROM) or a flash memory.

6. (Original) The device as claimed in the claim 4, wherein the signal status detected by the detection control unit is an output signal status of a BIOS.

7. (Original) The device as claimed in the claim 4, wherein the signal status detected by the detection control unit is a trigger signal status.

Claims 8-9. (Canceled)

10. (Original) The device as claimed in the claim 4, wherein the clock setting value stored in the memory unit is set by a BIOS.

11. (Original) The device as claimed in the claim 4, wherein the clock setting value stored in the memory unit is a fixed value.